

AMENDMENT TO THE CLAIMS

IN THE CLAIMS:

Please amend the claims to read as follows:

1. (Thrice amended) A circuit comprising:

a differential sense circuit;

a latch, said latch comprising cross coupled inverters;

said differential sense circuit and said latch being coupled so as to form a differential sense latch such that, in operation, an electronic signal stored in said latch is retained for at least one clock cycle.

REMARKS

The above-referenced patent application has been reviewed in light of the Final Office Action, dated December 26th, 2002, in which: The drawings are objected to in reference to claims 17 and 19; claims 1, 3-6, 10, 16, 20 and 25 are rejected under 35 U.S.C. 102(e) as being anticipated by Takahashi (hereinafter "Takahashi", US Patent No. 6,037,824); claims 1, 3 and 4 are rejected under 35 U.S.C. 102(b) as being anticipated by admitted prior art, figure 1 of the present application; claims 17, 18, and 20-24 are rejected under 35 U.S.C. 102(e) as being anticipated by Ohshima et al.(hereinafter "Ohshima", US Patent No. 6,163,501); claims 17, 18, and 20-24 are rejected under 35 U.S.C. 102(e) as being anticipated by Yang et al.(hereinafter "Yang", US Patent No. 6,239,624); and claims 7 and 17-24 are rejected under 35 U.S.C 103(a) as being unpatentable over Takahashi. Claims 1 and 3-25 are now pending the above-referenced patent application. Claim 1 has been amended. Reconsideration of the above-referenced